

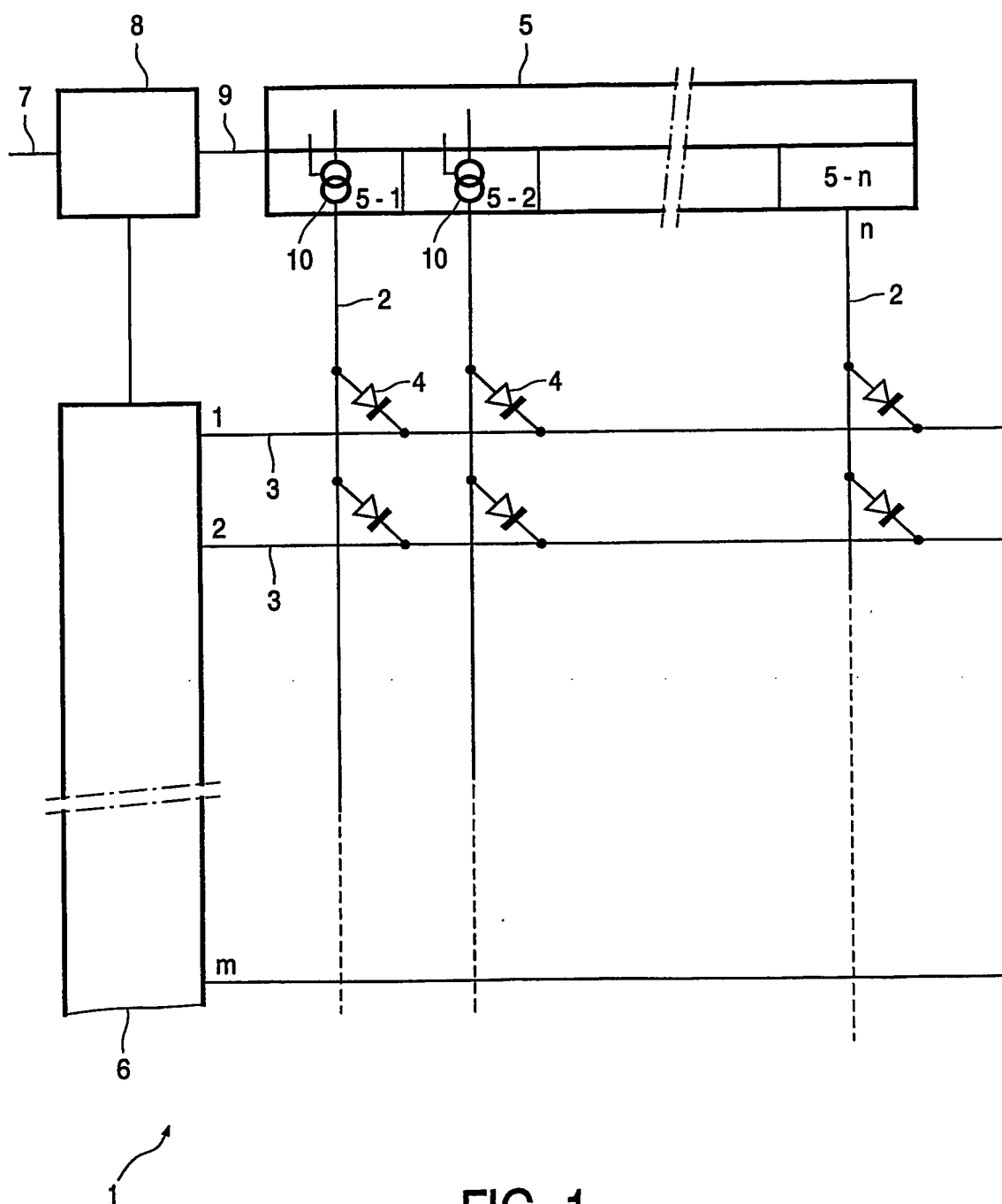
$\frac{1}{4}$ 

FIG. 1

The circuit diagram shows a feedback loop. A current I_i flows through a resistor 23 and a transistor 13. The emitter of transistor 13 is connected to the base of transistor 14. The collector of transistor 14 is connected to the base of transistor 19. The emitter of transistor 19 is connected to the collector of transistor 13. The collector of transistor 19 is connected to a resistor 20 and a diode 4. The diode 4 is connected to the base of transistor 11. The emitter of transistor 11 is connected to the base of transistor 14. The collector of transistor 11 is connected to a resistor 22 and a current source 16. The current source 16 is connected to the base of transistor 13. The output current I_o flows through a resistor 24 and the collector of transistor 19. The circuit is powered by a positive supply rail at the top and a negative supply rail at the bottom.

FIG. 3

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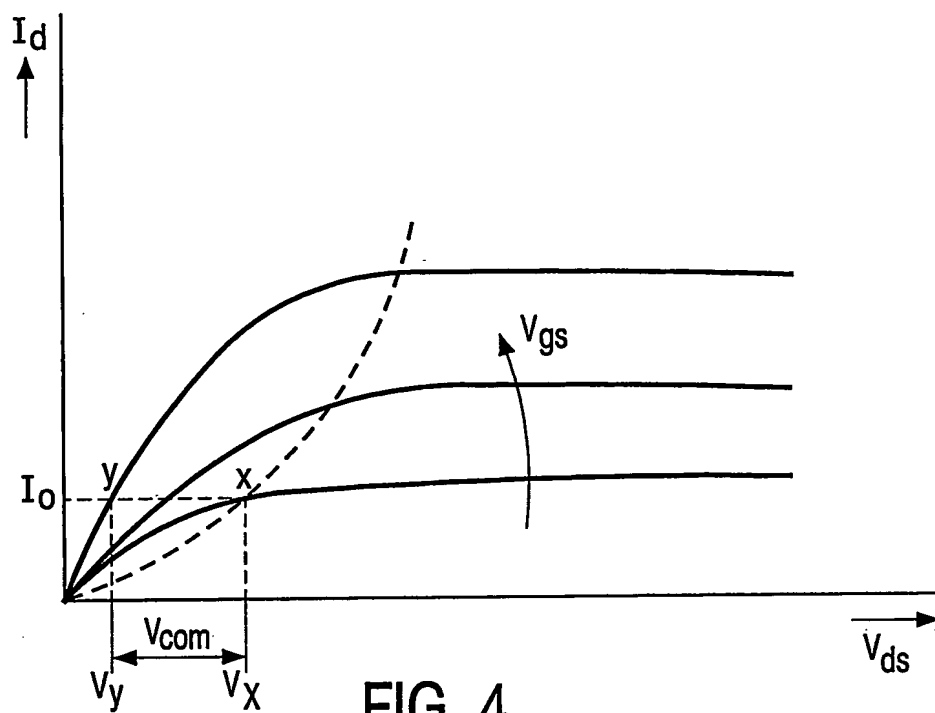


FIG. 4

